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☐ 1.   Document ID: US 20040003152 A1

Using default format because multiple data bases are involved.

L7: Entry 1 of 23   File: PGPB   Jan 1, 2004

PGPUB-DOCUMENT-NUMBER: 20040003152
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040003152 A1

TITLE: Backplane architecture for a data server

PUBLICATION-DATE: January 1, 2004

INVENTOR-INFORMATION:

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US-CL-CURRENT: 710/100; 709/219

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D |

☐ 2.   Document ID: US 20030221019 A1
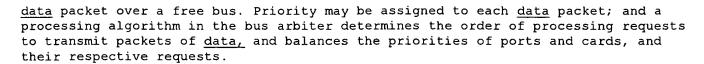
L7: Entry 2 of 23   File: PGPB   Nov 27, 2003

DOCUMENT-IDENTIFIER: US 20030221019 A1
TITLE: Media bus interface arbitration for a data server

Abstract Paragraph:
A data server for use in networks where media data are transmitted in packet form
comprises at least one card shelf containing at least a bus controller card, a
plurality of media processor cards, and a backplane. The backplane has a plurality
of media buses, which carry data in packet form among the cards installed on the
shelf. Each card has at least one bidirectional port with its own unique identity,
and each bidirectional port outputs flow control information as to whether that
port can or cannot accept packets of data that are intended to be sent to it, as
well as transmit requests to transmit packets of data from that port. Each card
outputs flow control information for each bus. A bus arbiter reviews each request
to transmit and each flow control message, and grants requests to transmit media
data packets only when the intended destination address is free to receive a media

data packet over a free bus. Priority may be assigned to each data packet; and a processing algorithm in the bus arbiter determines the order of processing requests to transmit packets of data, and balances the priorities of ports and cards, and their respective requests.

Summary of Invention Paragraph:
[0001] This invention relates to data servers for use in networks where media are transmitted in packet form. In particular, the present invention relates to data servers which are configured as a media server, wherein a shelf arrangement that includes a plurality of media processor cards, and a plurality of media buses on the backplane, is provided. The present invention particularly provides for arbitration as to the manner in which the control of packets of data from various cards within the data server shall be controlled. The data server may, however, function as a media server or a media gateway, particularly in telephony networks, video conferencing networks, and the like.

Summary of Invention Paragraph:
[0014] The data server comprises at least one card shelf that contains at least a first card having a bus controller function, a plurality of media processor cards, and a backplane.

CLAIMS:

1. A data server for use in networks where media are transmitted in packet form, where such media are chosen from the group consisting of packetized audio data, packetized video data, packetized control data, packetized information data, and combinations thereof, said data server comprising: at least a first card shelf containing a card having a bus controller function, a plurality of media processor cards, and a backplane; wherein said backplane includes a media bus group having a plurality of media buses therein; wherein said plurality of media buses are adapted to carry media data in packet form among the cards installed on said shelf; wherein each media processor card on said shelf is assigned a specific address identity; there being at least one bidirectional port on each media processor card, from and to which packets of media data may be transmitted; wherein each bidirectional port on each media processor card has its own identity on its respective card; wherein, when any bidirectional port on any media processor card wishes to send a packet of media data to another bidirectional port on any media processor card, it issues a request to transmit that packet of data; wherein the request to transmit a packet of media data includes the identity of the intended receiving card, and the identity of the receiving bidirectional port on the intended receiving card; wherein each bidirectional port on each media processor card outputs flow control information as to whether that bidirectional port can or cannot accept packets of media data that are intended to be sent to it at any instant in time; wherein each media processor card outputs flow control information for each bus in said media bus group as to whether that card can or cannot accept packets of media data that are intended to be sent to it over each respective bus at any instant in time; wherein said data server further includes a bus arbiter on said at least a first card having said bus controller function, the purpose of said bus arbiter being to review the flow control status of each output position and each bidirectional port thereat, to review the flow control status of each bus in said media bus group, to process requests to transmit packets of data from each bidirectional port on each media processor card, and to grant requests to transmit packets of media data only for those packets of media data that are intended to be transmitted to destination addresses that are not flow controlled, where the grants are given only for transmission over buses that are not flow controlled; and wherein all ports and all cards that have received grants to transmit packets of media data will place their specified packets of media data onto the respective granted buses, starting at the same instant in time.

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☐ 3.   Document ID: US 20030220964 A1

L7: Entry 3 of 23   File: PGPB   Nov 27, 2003

DOCUMENT-IDENTIFIER: US 20030220964 A1
TITLE: Data server

Detail Description Paragraph:
[0061] Typically, a data server in keeping with the present invention, configured
as a media server as indicated in FIG. 1, will include a single shelf controller
card 12, and a plurality of media processor cards 14. The shelf controller card and
media processor cards are plugged into a card shelf, in well known manner.

CLAIMS:

24. The data server of claim 4, wherein a plurality of media processors cards are
on said shelf.

32. The data server of claim 24, further comprising at least one further media
processor card, wherein said at least one further media processor card functions as
a backup media processor for any other one of said plurality of media processor
cards.

☐ 4.   Document ID: US 20030112758 A1

L7: Entry 4 of 23   File: PGPB   Jun 19, 2003

DOCUMENT-IDENTIFIER: US 20030112758 A1
TITLE: Methods and systems for managing variable delays in packet transmission

CLAIMS:

15. A media processing system for transmitting, receiving, and processing media
across networks wherein the media processing system has substantially reduced
jitter experienced by data packets in transmission from a transmitter to a
receiver, comprising: a plurality of media processors wherein the media processor
is capable of processing media; a plurality of packet processors in communication
with at least one of said media processors wherein the packet processor is capable
of packetizing processed media; a host processor in communication with at least one
said packet or media processors; and a playout buffer, implemented in either the
media processor or packet processor, having a buffer size equal to a function of a
first variance and a second variance and using a delay equal to a function of a
mean delay and the first variance wherein said mean delay, first variance and said
second variance are determined from a packet delay histogram representative of

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delays experienced by data packets in transmission from a transmitter to a receiver.

20. A media processing system for transmitting, receiving, and processing media across networks, comprising: a plurality of media processors, each of said media processors having a plurality of processing layers wherein each processing layer has at least one processing unit, at least one program memory, and at least one data memory, each of said processing unit, program memory, and data memory being in communication with one another; a plurality of packet processors in communication with at least one of said media processors wherein each of said packet processors is capable of packetizing processed media; a host processor in communication with at least one of said plurality of packet processors or at least one of said plurality of media processors; and a playout buffer, implemented in either the at least one of said plurality of packet processors or at least one of said plurality of media processors, having a buffer size equal to a function of a first variance and a second variance wherein said first variance and said second variance are determined from a packet delay histogram representative of delays experienced by data packets in transmission from a transmitter to a receiver.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 5. Document ID: US 20030105799 A1

L7: Entry 5 of 23   File: PGPB   Jun 5, 2003

DOCUMENT-IDENTIFIER: US 20030105799 A1
TITLE: Distributed processing architecture with scalable processing layers

CLAIMS:

12. A media gateway for the processing of data and communication of data across a plurality of networks, comprising: a plurality of media processors, each of said media processors having a plurality of processing layers wherein each processing layer has at least one processing unit, at least one program memory, and at least one data memory, each of said processing unit, program memory, and data memory being in communication with one another, wherein at least one processing unit in at least one of said processing layers performs echo cancellation functions on received data, wherein at least one processing unit in at least one of said processing layers performs encoding or decoding functions on received data, and wherein a task scheduler is adapted to receive a plurality of tasks from a source and distribute said tasks to the processing layers; a plurality of packet processors in communication with at least one of said media processors wherein the packet processor is adapted to packetize processed data; and a host processor in communication with at least one said packet or media processors.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

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☐ 6. Document ID: US 20030056172 A1

L7: Entry 6 of 23   File: PGPB   Mar 20, 2003

DOCUMENT-IDENTIFIER: US 20030056172 A1
TITLE: Method and system for mapping dialog definitions to multiple media

Summary of Invention Paragraph:
[0010] In yet a further aspect of the present invention, there is provided a system
for medium independent notification of a user via a user notification device
comprising the elements described in the previous aspect, and further including at
least one second medium processor for receiving a medium dependent description of a
reply from the user notification device, at least one medium dependent description
to medium independent description translator operatively connected to the at least
one second medium processor, said translator for receiving and translating a medium
dependent description of the reply to a medium independent description of the
reply, and operatively connected to the medium independent processor, for providing
the medium independent processor the medium independent description of the reply
and an action processor, operatively attached to the medium independent processor
and the at least one data source, for acting upon instructions contained in the
medium independent description of the reply, the instructions received by said
action processor from the medium independent processor, and for modifying the state
of the at least one data source. In embodiments of the present aspect there are a
plurality data sources, a medium independent processor that generates an extensible
markup language based medium independent description of the notification message, a
plurality of medium independent description to medium dependent description
translators which can be uniquely associated with a plurality of medium processors,
said translators and processors optionally being combined into a medium independent
description to medium dependent processor. Other embodiments of the present
invention include a plurality of second medium processors which can be uniquely
associated with to a medium dependent description to medium independent description
translator, said medium dependent description to medium independent description
translator optionally being combined into a medium dependent description to medium
independent description processors. In alternate embodiments the second medium
processor and the at least one medium processor are the same. In another alternate
embodiment the at least one second medium processor receives a medium dependent
description of a reply from a user notification device, wherein the user
notification device is distinct from the notification device to which the medium
dependent description of the notification message is transmitted.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D |

☐ 7. Document ID: US 20020106191 A1

L7: Entry 7 of 23   File: PGPB   Aug 8, 2002

DOCUMENT-IDENTIFIER: US 20020106191 A1
TITLE: Systems and methods for creating a video montage from titles on a digital
video disk

Detail Description Paragraph:
[0036] Additionally, media processing system 31 can include multiple media

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processors 32 to handle a variety of simultaneous <u>data</u> streams. The <u>multiple media processors</u> 32 can be incorporated on a single chip or implemented using multiple chips. It should thus be recognized that a single <u>data</u> stream and multiple <u>data</u> streams may be manipulated and/or displayed in accordance with the present invention.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D‹ |

☐ 8. Document ID: US 20020090201 A1

L7: Entry 8 of 23   File: PGPB   Jul 11, 2002

DOCUMENT-IDENTIFIER: US 20020090201 A1
TITLE: Systems and methods for creating a single video frame with one or more interest points

Detail Description Paragraph:
[0031] Additionally, media processing system 31 can include <u>multiple media processors</u> 32 to handle a variety of simultaneous <u>data</u> streams. The <u>multiple media processors</u> 32 can be incorporated on a single chip or implemented using multiple chips. It should thus be recognized that a single <u>data</u> stream and multiple <u>data</u> streams may be manipulated and/or displayed in accordance with the present invention.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D‹ |

☐ 9. Document ID: US 20020089519 A1

L7: Entry 9 of 23   File: PGPB   Jul 11, 2002

DOCUMENT-IDENTIFIER: US 20020089519 A1
TITLE: Systems and methods for creating an annotated media presentation

Detail Description Paragraph:
[0031] Additionally, media processing system 31 can include <u>multiple media processors</u> 32 to handle a variety of simultaneous <u>data</u> streams. The <u>multiple media processors</u> 32 can be incorporated on a single chip or implemented using multiple chips. It should thus be recognized that a single <u>data</u> stream and multiple <u>data</u> streams may be manipulated and/or displayed in accordance with the present invention.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D‹ |

☐ 10. Document ID: US 6702767 B1
L7: Entry 10 of 23   File: USPT   Mar 9, 2004

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DOCUMENT-IDENTIFIER: US 6702767 B1
TITLE: Multisensory stimulation system and method

Detailed Description Text (52):
As shown further in FIG. 12, the processor 260 may be connected to an interactive
media controller 270 which may be designed to allow control using pre-prepared
software programs. The multimedia controller 270 is connected to a plurality of
media processors and/or players, media synthesizers and media databases 271, 272,
and 273. The media processors and players 271 may include multimedia playback
systems and/or multimedia storage or playback devices which are available locally
or over a network. Such devices may include hard drives, DVD, DVD-ROM and CD-ROM
drives, VCRs, tape players, hi-fi audio equipment, multi-track audio cards, MIDI
and synthesizer-type playback devices, dual monitor video cards and even film
projection devices. The media processors and players may also include media mixers,
routers and switches, DSPs, converters and amplifiers to combine, modify,
transform, alter or adjust any media signal or format for final media output. By
way of example, the processors and players can manipulate existing files from the
media program database. In this manner changes can be made such as adjusting the
volume or adding reverb or echoes to recorded sound files, reversing the color
output in video, lightening or darkening existing or generated images, combining
two images together on one screen, etc.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 11. Document ID: US 6449378 B1

L7: Entry 11 of 23   File: USPT   Sep 10, 2002

DOCUMENT-IDENTIFIER: US 6449378 B1
TITLE: Data processing apparatus and method and storage medium

Detailed Description Text (41):
If an arrangement for moving image compression processing, electronic water-mark
information embedding processing, and encryption processing is integrated into one
circuit (media processor 62) as in this embodiment, further reductions in the size
and cost of the apparatus can be attained. In addition, if a plurality of media
processors are connected in parallel and controlled integrally, a further increase
in operation speed can be attained.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 12. Document ID: US 6442758 B1

L7: Entry 12 of 23   File: USPT   Aug 27, 2002

DOCUMENT-IDENTIFIER: US 6442758 B1
TITLE: Multimedia conferencing system having a central processing hub for
processing video and audio data for remote users

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Detailed Description Text (31):
From the above, it will be seen that the media processor card 34 provides a number of important functions. In a typical configuration, the video and audio data from two bi-directional video/audio ports--either MPEG or other compression protocol, or analog--are handled. Indeed, up to four bi-directional video/audio ports on a single media processor card may be handled. Accordingly, by providing a plurality of media processor cards, the number of participants in any multimedia conference and, indeed, the number of multimedia conferences, that may be handled by the central processing hub can be seen to be quite large. Moreover, as will be described hereafter, a number of media processor cards may be linked together by way of the bus controller card when functioning as a bus extender card. In any event, the media processor card functions in real-time, to provide real-time processing of video and audio signals.

Detailed Description Text (37):
Having regard to the interface between the packet bus 26 and the media processor card 34, the packet bus interface module 100 will sort packets on the packet bus 26 which are addressed to the particular media processor card 34 under consideration-- it being understood, of course, that in a typical installation there are a plurality of media processor cards 34. The data are directed to the control processor 104, and transmitted back to the packet bus 26 under the control of the bus arbitrator 105. In operation, the packet bus interface 100 handles the packet bus arbitration process under the control of the arbitrator 105 via a secondary bus 107. For incoming packets from the packet bus 26, the packet backplane interface 100 will indicate to the packet bus 26 when it is ready to receive packets; and, for outgoing packets, packets will only be transmitted to the packet bus 26 when it is noted that sufficient bandwidth on the packet bus 26 is available. Moreover, the packet backplane interface 100 monitors all received packets from the packet bus 26 for bit errors, and it makes the error count available to the control processor 104.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 13.   Document ID: US 6434253 B1

L7: Entry 13 of 23   File: USPT   Aug 13, 2002

DOCUMENT-IDENTIFIER: US 6434253 B1
TITLE: Data processing apparatus and method and storage medium

Detailed Description Text (101):
In this case, the overall image processing apparatus need not be implemented by one media processor. An arrangement in which an image processing apparatus is partially implemented by a media processor and an arrangement in which time-divisional processing is performed by using a plurality of media processors fall within the spirit and scope of the present invention.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

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☐ 14.   Document ID: US 6052713 A

L7: Entry 14 of 23   File: USPT   Apr 18, 2000

DOCUMENT-IDENTIFIER: US 6052713 A
TITLE: Technique for transmitting/storing multi sub data

Brief Summary Text (10):
The BSM used as a main processor in the digital cellular system is a system for
managing a plurality of medium processors BSC and sub processors BTS. The BSM
combines and analyzes various data corresponding to events generated in each of a
plurality of base stations. Several kinds of multi data to be processed by the BSM
are data to be transmitted from a plurality of base stations. Accordingly, it
requires that sub data transmitted from the base station are controlled
efficiently.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 15.   Document ID: US 6006318 A

L7: Entry 15 of 23   File: USPT   Dec 21, 1999

DOCUMENT-IDENTIFIER: US 6006318 A
TITLE: General purpose, dynamic partitioning, programmable media processor

Brief Summary Text (28):
Yet another aspect of the invention includes a system for unified media processing
having a plurality of general purpose media processors, where each media processor
is operable at substantially peak data rates and has a dynamically partitioned
execution unit and a high bandwidth interface for communicating to memory and
input/output elements to supply data to the media processor at substantially peak
rates. A bidirectional communication fabric is provided, to which the plurality of
media processors are coupled, to transmit and receive at least one media stream
comprising presentation, transmission, and storage media information. The bi-
directional communication fabric preferably comprises a fiber optic network, and a
subset of the plurality of media processors comprise network servers.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 16.   Document ID: US 5822603 A

L7: Entry 16 of 23   File: USPT   Oct 13, 1998

DOCUMENT-IDENTIFIER: US 5822603 A
TITLE: High bandwidth media processor interface for transmitting data in the form
of packets with requests linked to associated responses by identification data

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Brief Summary Text (28):
Yet another aspect of the invention includes a system for unified media processing
having a plurality of general purpose media processors, where each media processor
is operable at substantially peak data rates and has a dynamically partitioned
execution unit and a high bandwidth interface for communicating to memory and
input/output elements to supply data to the media processor at substantially peak
rates. A bi-directional communication fabric is provided, to which the plurality of
media processors are coupled, to transmit and receive at least one media stream
comprising presentation, transmission, and storage media information. The bi-
directional communication fabric preferably comprises a fiber optic network, and a
subset of the plurality of media processors comprise network servers.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 17. Document ID: US 5809321 A

L7: Entry 17 of 23　　　　　　　　　File: USPT　　　　　　　　　Sep 15, 1998

DOCUMENT-IDENTIFIER: US 5809321 A
TITLE: General purpose, multiple precision parallel operation, programmable media
processor

Brief Summary Text (28):
Yet another aspect of the invention includes a system for unified media processing
having a plurality of general purpose media processors, where each media processor
is operable at substantially peak data rates and has a dynamically partitioned
execution unit and a high bandwidth interface for communicating to memory and
input/output elements to supply data to the media processor at substantially peak
rates. A bi-directional communication fabric is provided, to which the plurality of
media processors are coupled, to transmit and receive at least one media stream
comprising presentation, transmission, and storage media information. The bi-
directional communication fabric preferably comprises a fiber optic network, and a
subset of the plurality of media processors comprise network servers.

CLAIMS:

1. A system for unified media processing comprising:

a plurality of general purpose media processors, each media processor being
operable at sustained peak data rates and having a dynamically partitioned
execution unit, wherein a plurality of media data streams are concurrently
transmitted over a single data path and are dynamically partitioned according to an
elemental symbol width that is equal to or narrower than the data path, and having
a high bandwidth interface, the high bandwidth interface coupled to external memory
and input/output elements to receive and transmit data to the media processor at
substantially peak rates; and

a bi-directional communication fabric, the plurality of media processors coupled to
the bi-directional communication fabric to transmit and receive at least one media
stream comprising presentation, transmission, and storage media information; and

wherein each media processor further comprises dedicated memory and wherein the
each of the plurality of media processors can employ any unused portion of the
dedicated memory of another media processor in a shared manner to efficiently store
and retrieve presentation, transmission and storage media information at

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substantially peak <u>data</u> rates.

8. The system defined in claim 1, wherein the <u>plurality of media processors</u> can access an idle execution unit of another media processor in a shared manner to efficiently process presentation, transmission and storage media information at substantially peak <u>data</u> rates.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 18. Document ID: US 5794061 A

L7: Entry 18 of 23   File: USPT   Aug 11, 1998

DOCUMENT-IDENTIFIER: US 5794061 A
TITLE: General purpose, multiple precision parallel operation, programmable media processor

<u>Brief Summary Text</u> (28):
Yet another aspect of the invention includes a system for unified media processing having a <u>plurality of general purpose media processors,</u> where each media processor is operable at substantially peak <u>data</u> rates and has a dynamically partitioned execution unit and a high bandwidth interface for communicating to memory and input/output elements to supply <u>data</u> to the media processor at substantially peak rates. A bi-directional communication fabric is provided, to which the <u>plurality of media processors</u> are coupled, to transmit and receive at least one media stream comprising presentation, transmission, and storage media information. The bi-directional communication fabric preferably comprises a fiber optic network, and a subset of the <u>plurality of media processors</u> comprise network servers.

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 19. Document ID: US 5794060 A

L7: Entry 19 of 23   File: USPT   Aug 11, 1998

DOCUMENT-IDENTIFIER: US 5794060 A
TITLE: General purpose, multiple precision parallel operation, programmable media processor

<u>Brief Summary Text</u> (28):
Yet another aspect of the invention includes a system for unified media processing having a <u>plurality of general purpose media processors,</u> where each media processor is operable at substantially peak <u>data</u> rates and has a dynamically partitioned execution unit and a high bandwidth interface for communicating to memory and input/output elements to supply <u>data</u> to the media processor at substantially peak rates. A bi-directional communication fabric is provided, to which the <u>plurality of media processors</u> are coupled, to transmit and receive at least one media stream comprising presentation, transmission, and storage media information. The bi-directional communication fabric preferably comprises a fiber optic network, and a

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subset of the <u>plurality of media processors</u> comprise network servers.

☐ 20. Document ID: US 5742840 A

L7: Entry 20 of 23   File: USPT   Apr 21, 1998

DOCUMENT-IDENTIFIER: US 5742840 A
TITLE: General purpose, multiple precision parallel operation, programmable media processor

<u>Brief Summary Text</u> (28):
Yet another aspect of the invention includes a system for unified media processing having a <u>plurality of general purpose media processors,</u> where each media processor is operable at substantially peak <u>data</u> rates and has a dynamically partitioned execution unit and a high bandwidth interface for communicating to memory and input/output elements to supply <u>data</u> to the media processor at substantially peak rates. A bi-directional communication fabric is provided, to which the <u>plurality of media processors</u> are coupled, to transmit and receive at least one media stream comprising presentation, transmission, and storage media information. The bi-directional communication fabric preferably comprises a fiber optic network, and a subset of the <u>plurality of media processors</u> comprise network servers.

☐ 21. Document ID: US 5729214 A

L7: Entry 21 of 23   File: USPT   Mar 17, 1998

DOCUMENT-IDENTIFIER: US 5729214 A
TITLE: Condition reactive display medium

<u>Brief Summary Text</u> (25):
Further disclosed is a traffic control system for blocking off a portion of a transit lane or other area from ingress or egress comprising: a plurality of medium each having a means for extending and retracting a protrusion means; a plurality of medium transceiver means attached to each of said medium processor means for receiving control signals; a central processing means disposed at a remote location from said plurality of mediums having a transceiver means for transmitting a control signal for receipt by one or more said medium transceiver means, and for initiating extension or retraction of said protrusion means for one or more of said mediums. The control signal may be responsive to pre-programmed input regarding anticipated traffic conditions. The control signal may be digital in nature and may be comprised of numerous <u>data</u> blocks. Certain of the <u>data</u> blocks may be interpretable by certain of said <u>plurality of medium processors</u> as command signals with respect to the state of retraction or extension of said protrusion means. The <u>data</u> blocks may be interpretable by certain of said medium processors as key codes for unlocking said medium processor so as to permit control over the extension or

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retraction of said protrusion means in accordance with the command portion of the signal.

☐ 22. Document ID: US 5684654 A

L7: Entry 22 of 23   File: USPT   Nov 4, 1997

DOCUMENT-IDENTIFIER: US 5684654 A
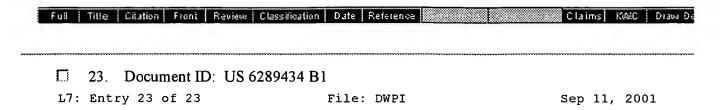TITLE: Device and method for storing and retrieving data

Abstract Text (1):
A data storage unit includes multiple media processors that are each moveable between a transfer position and a corresponding standby position, and are able to receive a storage medium. A shuttle assembly moves a selected one of the media processors between the transfer position and the corresponding standby position. A magazine is provided with a plurality of compartments that are each able to receive a storage medium. A magazine drive assembly moves the magazine so as to move a selected one of the compartments into the transfer position. A transfer assembly transfers a storage medium between the selected media processor and the selected compartment while the selected media processor and the selected compartment are in the transfer position. A controller controls the operations of the shuttle assembly, magazine drive assembly, and transfer assembly.

Brief Summary Text (8):
In accordance with the present invention, a data storage unit is provided that is usable with storage media. The storage unit includes multiple media processors that are each moveable between a transfer position and a corresponding standby position, and are able to receive a storage medium. A shuttle assembly moves a selected one of the media processors between the transfer position and the corresponding standby position. A magazine is provided with a plurality of compartments that are each able to receive a storage medium. A magazine drive assembly moves the magazine so as to move a selected one of the compartments into the transfer position. A transfer assembly transfers a storage medium between the selected media processor and the selected compartment while the selected media processor and the selected compartment are in the transfer position. A controller controls the operations of the shuttle assembly, magazine drive assembly, and transfer assembly.

Detailed Description Text (5):
Thus, storage unit 10 provides increased data-transfer speed over existing storage devices by providing multiple media processors 40a and 40b. For example, while a storage-medium transfer is occurring relative to one media processor, the other media processor can be storing data on or retrieving data from another storage medium. Or, unit 10 can off-line copy the data stored on one storage medium directly to another storage medium without having to temporarily store the data on-line, i.e., in the attached computer system's memory. Furthermore, unit 10 can generate simultaneous back-up files by using two media processors to simultaneously write the same data to two different storage media. Additionally, by moving both the selected compartment 26 and the selected media processor into a single transfer position, transfer assembly 30 can be designed to move in a single dimension. Such single-dimension movement reduces the complexity of transfer assembly 30 from that of existing transfer assemblies designed to move in multiple dimensions.

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| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

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TITLE: Data processing apparatus for television set, computer, processes memory
data in response to received instructions simultaneously with other operations

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

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